UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO. : 7,065,697 B2 APPLICATION NO.: 10/632207

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INVENTOR(S)

: Theodore Carter Briggs et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 4, lines 42-46, after "unit 12." delete "Each adjacent bit pair domain includes a data portion and a correction bit portion. The number of bits in an adjacent bit pair domain corresponds to the number of bits correctable by the error corrector 13 in the error correction unit 12.".

In column 10, line 58, in Claim 6, after "plurality" delete ",".

Signed and Sealed this

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Tenth Day of March, 2009

JOHN DOLL Acting Director of the United States Patent and Trademark Office